

AMENDMENTS TO THE CLAIMS

1. (Original) A method of fabricating a semiconductor device comprising steps of: forming a silicon layer to be in contact with at least either the upper surface or the lower surface of a first film having a contact angle of not more than about 45° with respect to molten silicon; and crystallizing said silicon layer after melting said silicon layer by heating said silicon layer with a continuously oscillated electromagnetic wave.

2. (Original) The method of fabricating a semiconductor device according to claim 1, wherein said first film has a smaller contact angle with respect to molten silicon than a silicon oxide film.

3. (Original) The method of fabricating a semiconductor device according to claim 2, wherein said first film includes at least either an SiN_x film or an SiCN film having a contact angle of not more than about 45° with respect to molten silicon.

4. (Original) The method of fabricating a semiconductor device according to claim 2, wherein said first film includes an SiC film.

5. (Withdrawn) The method of fabricating a semiconductor device according to claim 1, wherein

 said step of crystallizing said silicon layer includes a step of forming an absorption film either above or under said silicon layer through an insulating layer, and a step of applying a continuous-wave laser beam to said absorption film thereby making said absorption film generate heat and crystallizing said silicon layer through generated said heat.

6. (Withdrawn) The method of fabricating a semiconductor device according to claim 5, wherein

 said continuous-wave laser beam includes an infrared laser beam having a wavelength of at least about 0.75 μm and not more than about 2.0 μm .

7. (Withdrawn) The method of fabricating a semiconductor device according to claim 6, wherein

 said continuous-wave laser beam includes a continuous-wave YAG laser beam.

8. (Withdrawn) The method of fabricating a semiconductor device according to claim 5, wherein

 said absorption film consists of a material including Mo.

9. (Withdrawn) The method of fabricating a semiconductor device according to claim 5, further comprising a step of forming a gate electrode by patterning said absorption film after said step of forming said absorption film.

10. (Withdrawn) The method of fabricating a semiconductor device according to claim 5, wherein

 said step of forming said absorption film includes a step of previously patterning said absorption film to be employable as a light-shielding film for a pixel part of a display.

11. (Withdrawn) The method of fabricating a semiconductor device according to claim 10, wherein

 said step of previously patterning said absorption film to be employable as a light-shielding film for a pixel part of a display includes a step of patterning said absorption film in the form of a matrix.

12. (Currently Amended) The method of fabricating a semiconductor device according to claim 1, wherein

 said step of crystallizing said silicon layer includes a step of heating said silicon layer with a fundamental wave of said a continuous-wave laser beam.

13. (Withdrawn) The method of fabricating a semiconductor device according to claim 1, wherein

 said step of forming said silicon layer includes a step of forming said silicon layer to be in contact with the upper surface of said first film,

said method of fabricating a semiconductor device further comprising a step of forming said first film on a substrate through a buffer layer for relaxing heat transfer to said substrate in advance of formation of said silicon layer.

14. (Withdrawn) The method of fabricating a semiconductor device according to claim 13, wherein

said buffer layer includes a silicon oxide film.

15. (Withdrawn) The method of fabricating a semiconductor device according to claim 1, further comprising steps of:

forming a source/drain region on said silicon layer by implanting an impurity into said silicon layer, and

activating said impurity in said source/drain region with said continuously oscillated electromagnetic wave.

16. (Withdrawn) The method of fabricating a semiconductor device according to claim 15, further including a step of forming a patterned gate electrode on said silicon layer in advance of said step of forming said source/drain region on said silicon layer.

17. (Withdrawn) The method of fabricating a semiconductor device according to claim 15, further including a step of applying a bias voltage between either said source or drain region of said silicon layer and said absorption film.

18. (Original) The method of fabricating a semiconductor device according to claim 1, further comprising a step of forming roughness on the surface of said first film to be formed with said silicon layer in advance of said step of forming said silicon layer.

19. (Original) The method of fabricating a semiconductor device according to claim 18, wherein

 said step of forming said roughness includes a step of forming said roughness on the surface of said first film by etching the surface of said first film.

20. (Original) The method of fabricating a semiconductor device according to claim 1, wherein

 said first film having said contact angle of not more than about 45° with respect to molten silicon is an SiN_x film formed by plasma CVD.

21. (Original) The method of fabricating a semiconductor device according to claim 20, wherein

 said SiN_x film is formed by plasma CVD while setting the flow ratios of SiH_4 gas, NH_3 gas and N_2 gas to 2:1:100 to 2:2:100.